

FIG. 1

Row Reset Circuit

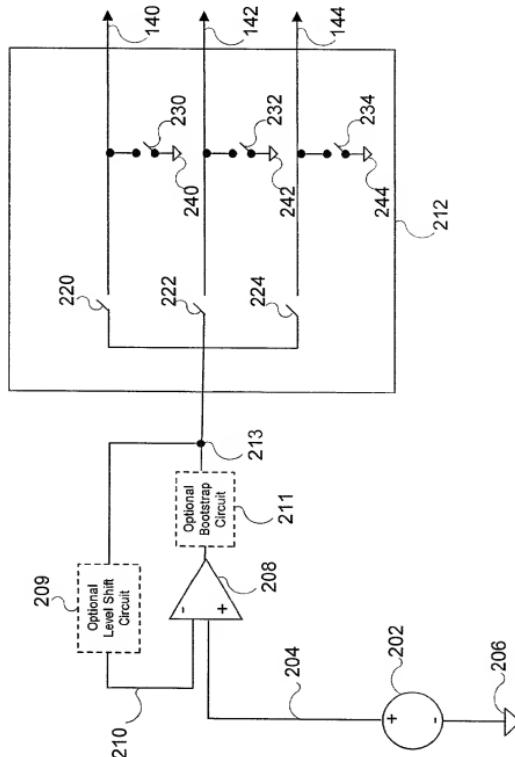


FIG. 2

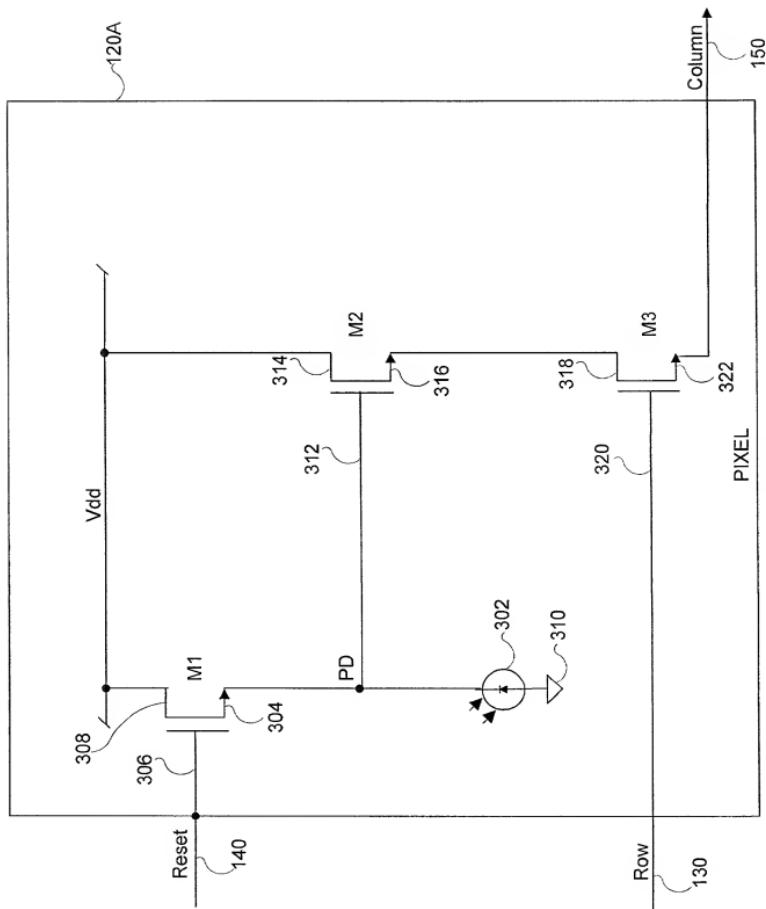


FIG. 3

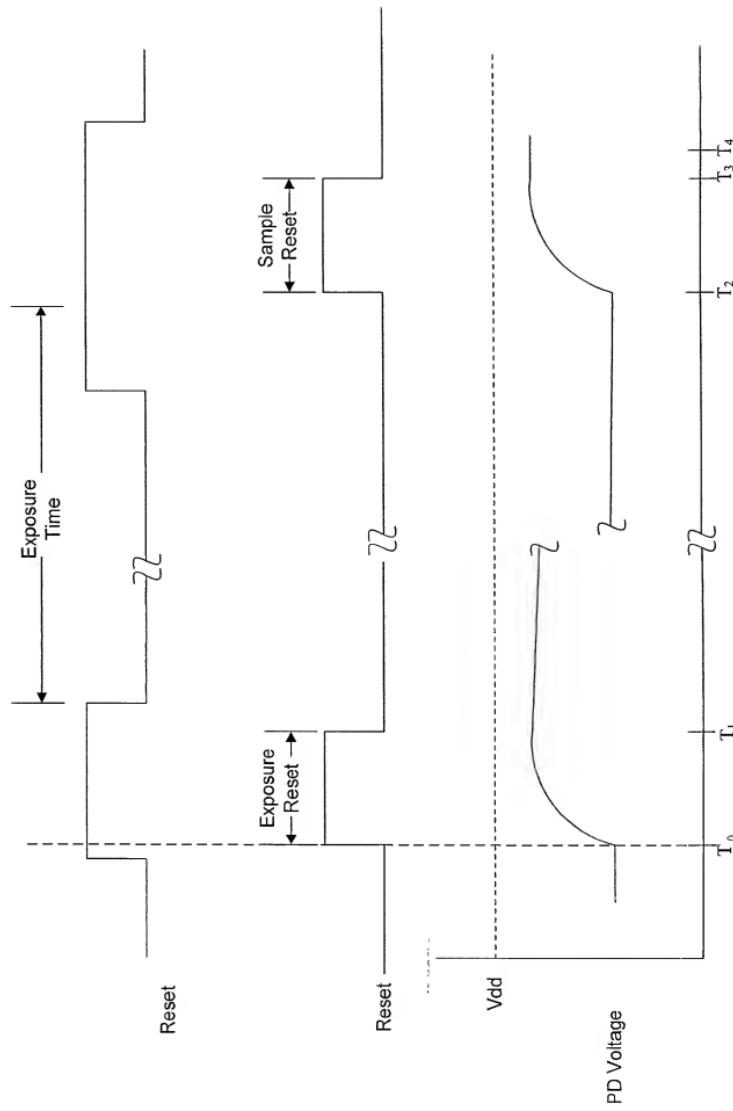


FIG. 4